

Challenges To The Design Of Integrated Systems In Future CMOS Technologies

In the future More-than Moore applications gain importance such as sensor interfaces, biomedical and automotive applications. They also use nanometer CMOS technologies, in which several new effects emerge. They have an impact on speed, noise and mismatch. Moreover the supply voltage is reduced to values below 1 Volt, creating new challenges for both analog and mixed-signal circuit design. In this 45 minutes presentation, an overview is given of these new applications, followed by a discussion of the limitations such as noise and distortion. A number of low-voltage amplifiers/filters configurations are then discussed, including Analog-to-Digital Converters.

Expert Speaker : Prof Willy Sansen

8th Jun 2010 | Hotel Amara | 0900-1200HR
165 Tanjong Pagar Road Singapore 088539



Prof. Willy Sansen is a member of several editorial and program committees of journals and conferences. He is cofounder and organizer of the workshops on Advances in Analog Circuit Design in Europe. He is a member of the executive and program committees of the IEEE ISSCC conference. He was program chair of the ISSCC-2002 conference and was President of the Solid-State Circuits Society in 2008/2009. He is a life-fellow of the IEEE.

SSIA Member & Guest : \$70.00 per person
IEEE SSCS Member : S\$70.00 per person
Non-member : \$85.00 per person

RSVP : secretariat@ssia.org.sg
Contact : Ms. Angela Yip
Tel : +65-62782538



SPYRO
TECHNOLOGY

Jointly Organized By IEEE Solid-State Circuits Singapore Chapter & Spyro Technology.

